

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Naofumi OHASHI *et al.*
Application No.: Unassigned (§53(b) Continuation of 10/369,716)
Filed: January 21, 2004
For: PROCESS FOR MANUFACTURING SEMICONDUCTOR
INTEGRATED CIRCUIT DEVICE
Expected Group: 2824
Expected Examiner: M. Lebentritt

CLAIM FOR PRIORITY

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

January 21, 2004

Sir:

Pursuant to the provisions of 35 USC § 119 and 37 CFR § 1.55, Applicants hereby claim the right of priority based on Japanese Patent Application No. 10-209857, filed in Japan on July 24, 1998.

A certified copy of the above-referred-to Japanese Patent Application was submitted in prior application Serial No. 09/356,707, filed July 20, 1999, on July 20, 1999.

Respectfully submitted,

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